



**THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant(s): Douglas D. Coolbaugh, et al. Examiner: Unassigned

Serial No: 10/711,130 Art Unit: Unassigned

Filed: August 26, 2004 Docket: BUR920040067US1 (17542)

Dated: November 29, 2004

For: METHOD OF ADJUSTING RESISTORS POST SILICIDE PROCESS

Commissioner for Patents  
United States Patent Office  
Alexandria, VA 23313-1450

**AMENDMENT UNDER 37 C.F.R. § 1.121**

Sir:

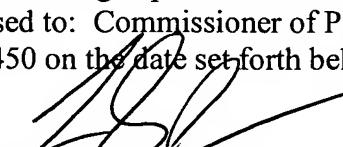
In response to the Notice to File Corrected Application Papers dated September 28, 2004, applicants submit the following amendments and remarks for entry of record in the above-identified patent application. A copy of the Notice to File Corrected Application Papers is attached hereto.

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**CERTIFICATE OF MAILING UNDER 37 C.F.R. §1.8(a)**

I hereby certify that this correspondence is being deposited with the United States Postal Service as first class mail in an envelope addressed to: Commissioner of Patents and Trademarks, P.O. Box 1450, Alexandria, VA 22313-1450 on the date set forth below.

Dated: November 29, 2004

  
Leslie S. Szivos, Ph.D.